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## INDIAN INSTITUTE OF TECHNOLOGY GANDHINAGAR (IITGN)

### Advertisement for Research Associate Position

26.02.2024

#### Job Description

Applications are invited for temporary position of Research Associate - I for the research project on “**Secure and Energy efficient Mixed domain Compute in memory-based AI accelerator Chip for Edge applications**” funded Ministry of Electronics and IT (MEITY) through the C2S project.

**Remuneration:** 58000/- (consolidated) p.m. + HRA

HRA will be provided as per institute norms if accommodation is not available within the institute.

#### Duration

The candidate will be offered a position for 12 months, which will be extended by 1 year based on the satisfactory performance of the candidate and availability of funds.

#### Essential Qualifications

Ph.D. degree in Electrical/Electronics/Electronics and communications/Electronics and Instrumentation/Computer Science and Engineering/Computer Science or related areas. Experienced candidates will be given priority. Doctoral work is expected to be aligned with the project requirements. The candidate should have at least 1 SCI journal.

#### Project Description

The candidate is expected to have excellent understanding of digital and analog circuits, and should have a thorough exposure to relevant VLSI tools and techniques. Candidates with excellent programming skills C/C++/Python, Verilog/System Verilog or equivalent, etc. and candidates with interest in computer architecture and having knowledge of system simulators will be given preference. Knowledge of VLSI Design and testing tools will be an advantage.

**Application procedure:** Interested candidates can apply [here](#). For any queries, please send an email to [joycee@iitgn.ac.in](mailto:joycee@iitgn.ac.in)

Last date of application: 7<sup>th</sup> March 2024.

Interview will be conducted in the online mode.

No TA/DA will be provided for interview.